

FEATURES

- 10µs Short Circuit Withstand
- High Thermal Cycling Capability
- High Current Density
- Isolated AISiC Base with AlN Substrates
- Pre-applied Thermal Interface Material

APPLICATIONS

- High Reliability Inverters
- Motor Controllers
- Smart Grid
- Traction Drives

The Powerline range of high power modules includes half bridge, chopper, dual, single and bi-directional switch configurations covering voltages from 1200V to 6500V and currents up to 2400A.

The DIM2400ESM17-RT500 is a single switch 1700V, trench gate, insulated gate bipolar transistor (IGBT) module with enhanced field stop and implantation technology. The IGBT has a wide reverse bias safe operating area (RBSOA) plus 10µs short circuit withstand. This device is optimised for traction drives and other applications requiring high thermal cycling capability.

The module incorporates an electrically isolated base plate and low inductance construction enabling circuit designers to optimise circuit layouts and utilise grounded heat sinks for safety.

ORDERING INFORMATION

Order As:

DIM2400ESM17-RT500

Note: When ordering, please use the complete part number

KEY PARAMETERS

V_{CES}		1700V
$V_{CE(sat)}$	(typ)	1.8V
I_C	(max)	2400A
$I_{C(PK)}$	(max)	4800A

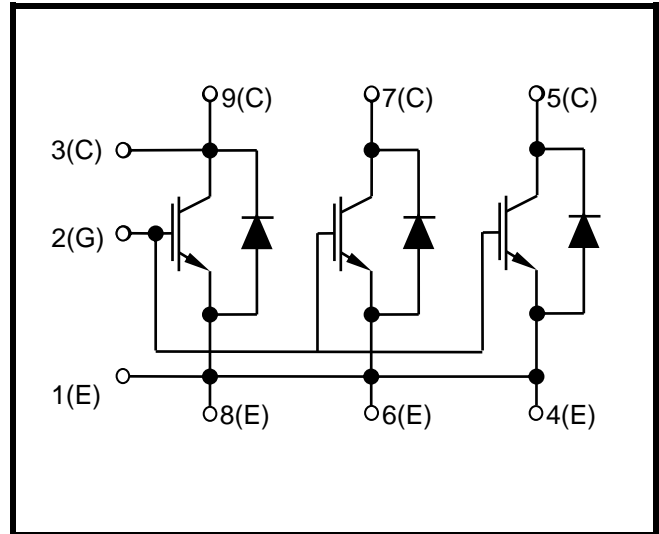
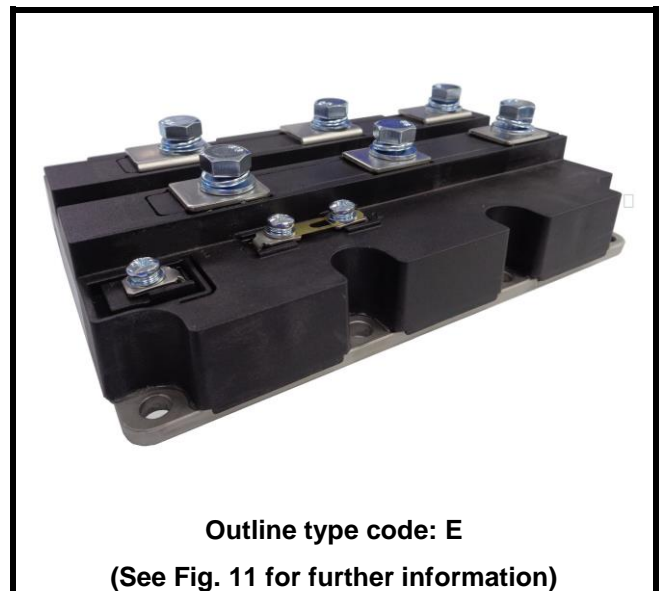


Fig. 1 Circuit configuration



Outline type code: E
(See Fig. 11 for further information)

Fig. 2 Package

ABSOLUTE MAXIMUM RATINGS

Stresses above those listed under ‘Absolute Maximum Ratings’ may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	1700	V
V _{GES}	Gate-emitter voltage		±20	V
I _C	Continuous collector current	T _c = 95°C, T _{vj} max = 175°C	2400	A
I _{C(RM)}	Peak collector current	t _p = 1ms,	4800	A
P _{max}	Max. transistor power dissipation	T _c = 25°C, T _{vj} = 175°C	18.3	kW
I ² t	Diode I ² t value	V _R = 0, t _p = 10ms, T _{vj} = 150°C	1240	kA ² s
V _{isol}	Isolation voltage – per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz, T _c = 25°C	4000	V
Q _{PD}	Partial discharge – per module	IEC1287, V ₁ = 1800V, V ₂ = 1300V, 50Hz RMS	10	pC

THERMAL AND MECHANICAL RATINGS

Internal insulation material:	AlN
Baseplate material:	AlSiC
Creepage distance: Terminal to heatsink	33mm
Creepage distance: Terminal to terminal	34mm
Clearance: Terminal to heatsink	20mm
Clearance: Terminal to terminal	20mm
CTI (Comparative Tracking Index):	>600

Symbol	Parameter	Test Conditions	Min	Typ.	Max	Units
R _{th(j-hs)}	Thermal resistance, junction to heatsink – IGBT	TIM mounting torque 5Nm, with pre-applied TIM	-	-	11.1	°C/kW
R _{th(j-hs)}	Thermal resistance, junction to heatsink – diode	TIM mounting torque 5Nm, with pre-applied TIM	-	-	12.9	°C/kW
T _j	Operating junction temperature	IGBT	-40	-	150	°C
		Diode	-40	-	150	°C
T _{stg}	Storage temperature range	-	-40	-	150	°C
	Screw torque	Mounting – M6	-	-	5	Nm
		Electrical connections – M4	-	-	2	Nm
		Electrical connections – M8	-	-	10	Nm

ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}C$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
I_{CES}	Collector cut-off current	$V_{GE} = 0V, V_{CE} = V_{CES}$			1	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{vj} = 125^{\circ}C$			40	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{vj} = 150^{\circ}C$			70	mA
I_{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$			1	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 90mA, V_{GE} = V_{CE}$	5.60	6.20	6.80	V
$V_{CE(sat)**}$	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 2400A$		1.80	2.20	V
		$V_{GE} = 15V, I_C = 2400A, T_{vj} = 125^{\circ}C$		2.20		V
		$V_{GE} = 15V, I_C = 2400A, T_{vj} = 150^{\circ}C$		2.30		
I_F	Diode forward current	DC		2400		A
I_{FM}	Diode maximum forward current	$t_p = 1ms$		4800		A
V_F	Diode forward voltage	$I_F = 2400A$		1.75	2.20	V
		$I_F = 2400A, T_{vj} = 125^{\circ}C$		1.80		V
		$I_F = 2400A, T_{vj} = 150^{\circ}C$		1.80		
C_{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 100kHz$		381		nF
Q_g	Gate charge	$\pm 15V$		28.4		μC
C_{res}	Reverse transfer capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 100kHz$		1.2		nF
L_M	Module inductance			6		nH
R_{CC+EE}	Module lead resistance, terminal - chip			55		$\mu\Omega$
R_{INT}	Internal transistor resistance			1		Ω
SC_{Data}	Short circuit current, I_{sc}	$T_j = 150^{\circ}C, V_{CC} = 1000V$ $t_p \leq 10\mu s, V_{GE} \leq 15V$ $V_{CE(max)} = V_{CES} - L^* \times di/dt$ IEC 60747-9		10800		A

Note:

* L is the circuit inductance + L_M

** indicates it is given at chip level

ELECTRICAL CHARACTERISTICS

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 2400A V _{GE} = ±15V V _{CE} = 900V L _S ~ 60nH	R _{G(OFF)} = 0.9Ω dv/dt = 2400V/μs		1870		ns
t _f	Fall time				280		ns
E _{OFF}	Turn-off energy loss				870		mJ
t _{d(on)}	Turn-on delay time		R _{G(ON)} = 0.9Ω di/dt = 7000A/μs		1135		ns
t _r	Rise time				295		ns
E _{ON}	Turn-on energy loss				570		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 2400A			500		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 900V			1400		A
E _{rec}	Diode reverse recovery energy	dI _F /dt = 7000A/μs			295		mJ

T_{case} = 125°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 2400A V _{GE} = ±15V V _{CE} = 900V L _S ~ 60nH	R _{G(OFF)} = 0.9Ω dv/dt = 2400V/μs		2050		ns
t _f	Fall time				430		ns
E _{OFF}	Turn-off energy loss				1090		mJ
t _{d(on)}	Turn-on delay time		R _{G(ON)} = 0.9Ω di/dt = 7000A/μs		1235		ns
t _r	Rise time				295		ns
E _{ON}	Turn-on energy loss				910		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 2400A			900		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 900V			1700		A
E _{rec}	Diode reverse recovery energy	dI _F /dt = 7000A/μs			640		mJ

T_{case} = 150°C unless stated otherwise

Symbol	Parameter	Test Conditions		Min	Typ.	Max	Units
t _{d(off)}	Turn-off delay time	I _C = 2400A V _{GE} = ±15V V _{CE} = 900V L _S ~ 60nH	R _{G(OFF)} = 0.9Ω dv/dt = 2400V/μs		2100		ns
t _f	Fall time				470		ns
E _{OFF}	Turn-off energy loss				1110		mJ
t _{d(on)}	Turn-on delay time		R _{G(ON)} = 0.9Ω di/dt = 7000A/μs		1255		ns
t _r	Rise time				300		ns
E _{ON}	Turn-on energy loss				930		mJ
Q _{rr}	Diode reverse recovery charge	I _F = 2400A			1100		μC
I _{rr}	Diode reverse recovery current	V _{CE} = 900V			1830		A
E _{rec}	Diode reverse recovery energy	dI _F /dt = 7000A/μs			675		mJ

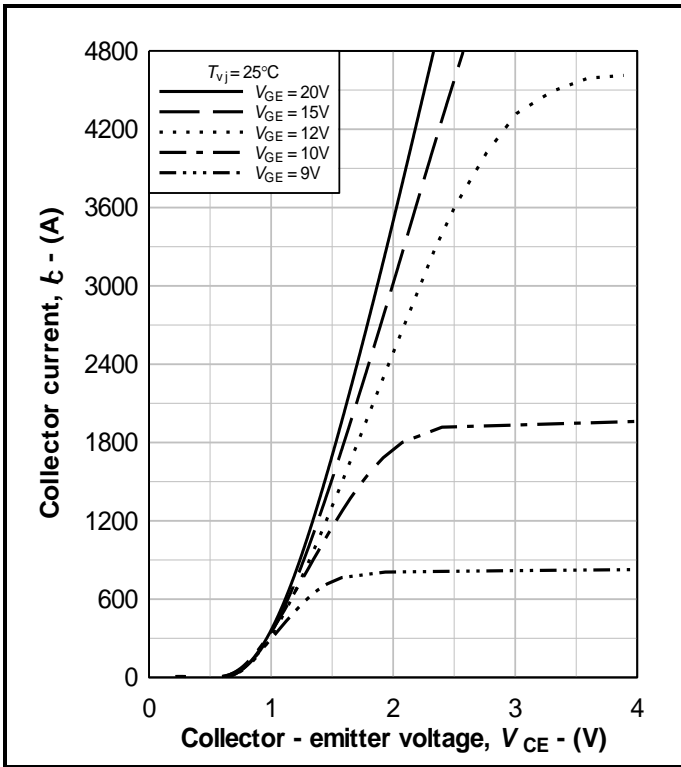


Fig. 3 Typical IGBT output characteristics

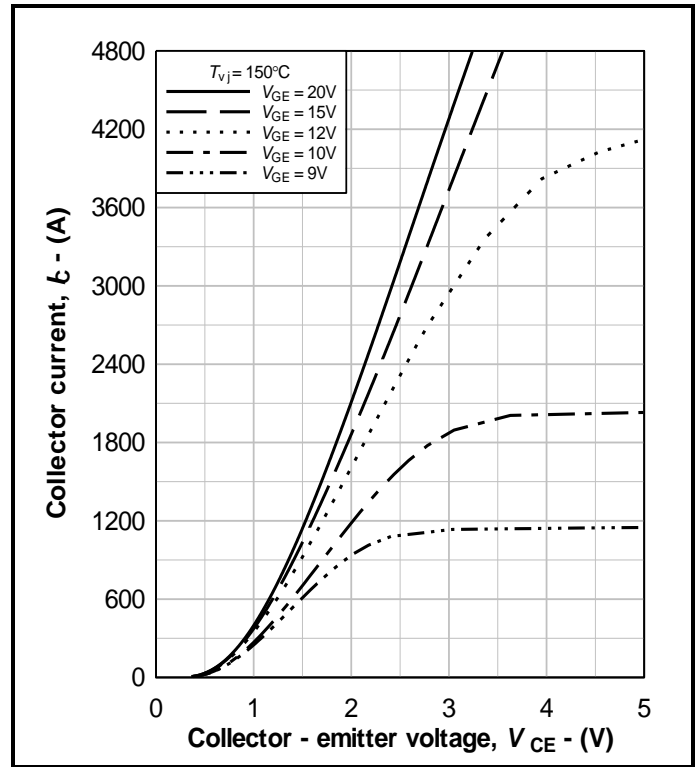


Fig. 4 Typical IGBT output characteristics

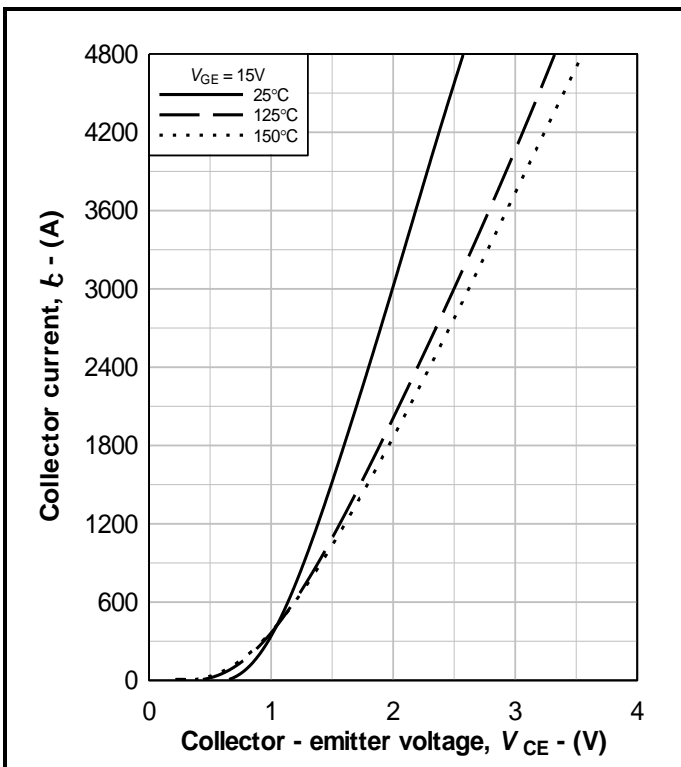


Fig. 5 Typical IGBT output characteristics, $I_c = f(V_{CE})$

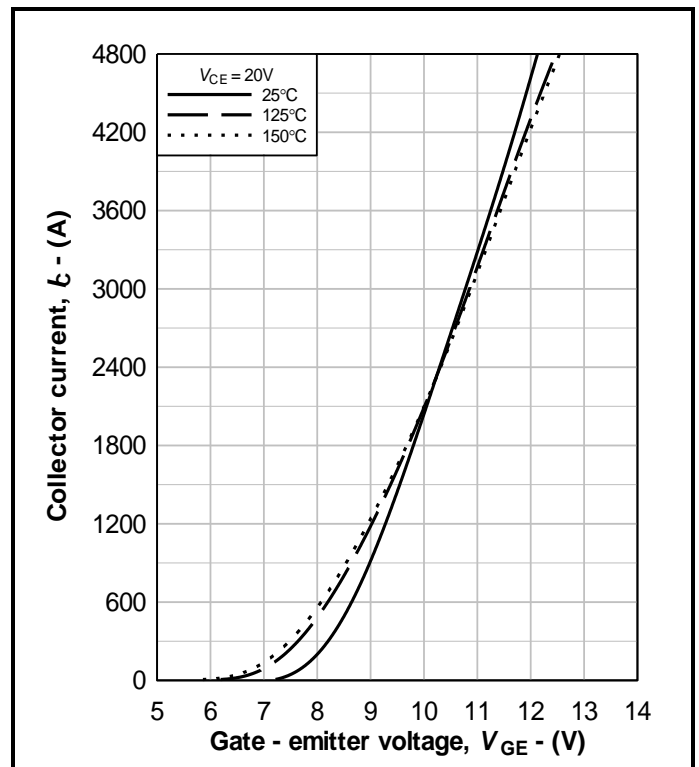


Fig. 6 Typical IGBT transfer characteristics, $I_c = f(V_{GE})$

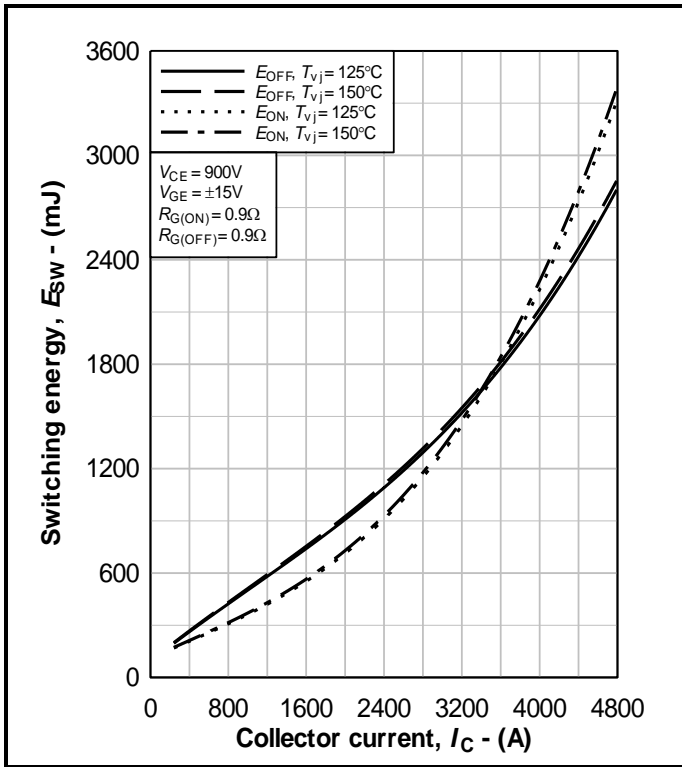


Fig. 7 Typical IGBT switching energy, $E_{ON} = f(I_C)$, $E_{OFF} = f(I_C)$

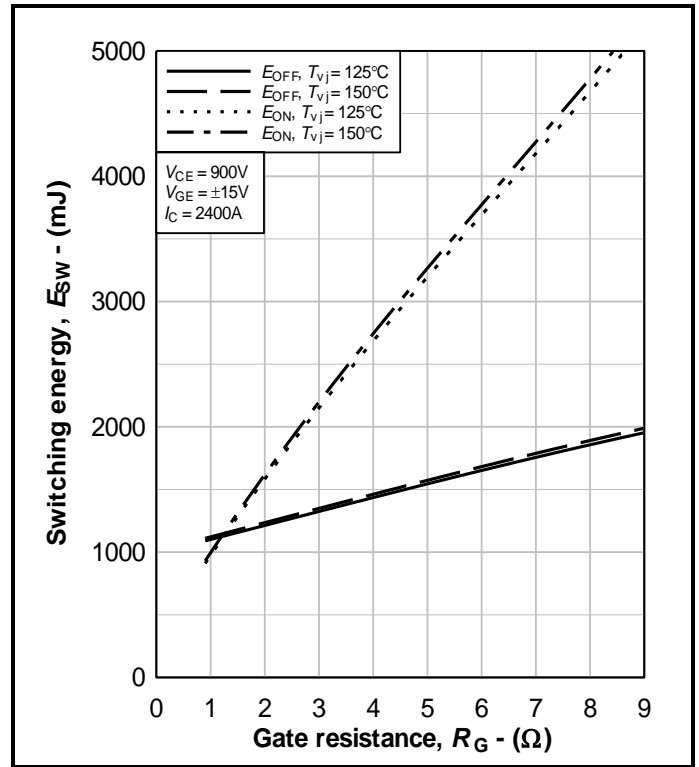


Fig. 8 Typical IGBT switching energy, $E_{ON} = f(R_G)$, $E_{OFF} = f(R_G)$

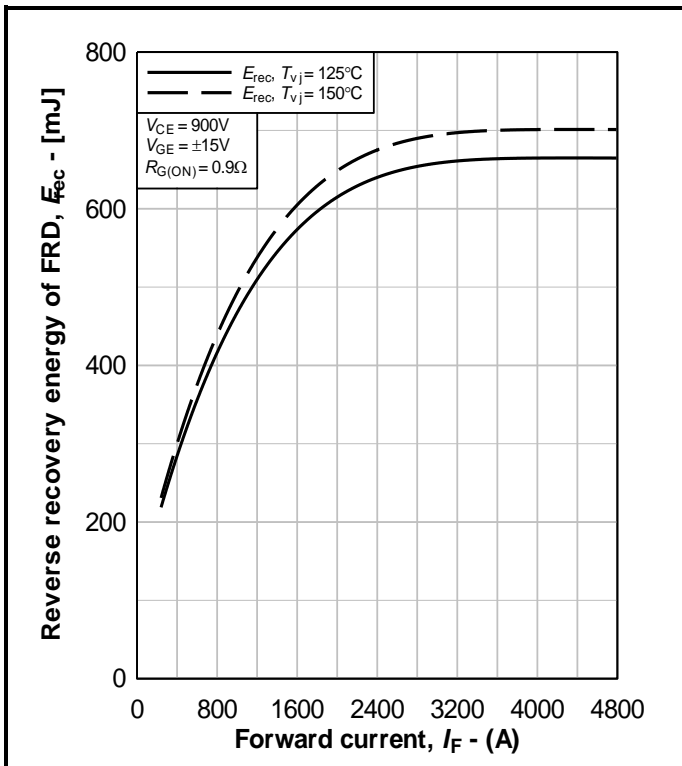


Fig. 9 Typical FRD E_{rec} , $E_{rec} = f(I_F)$

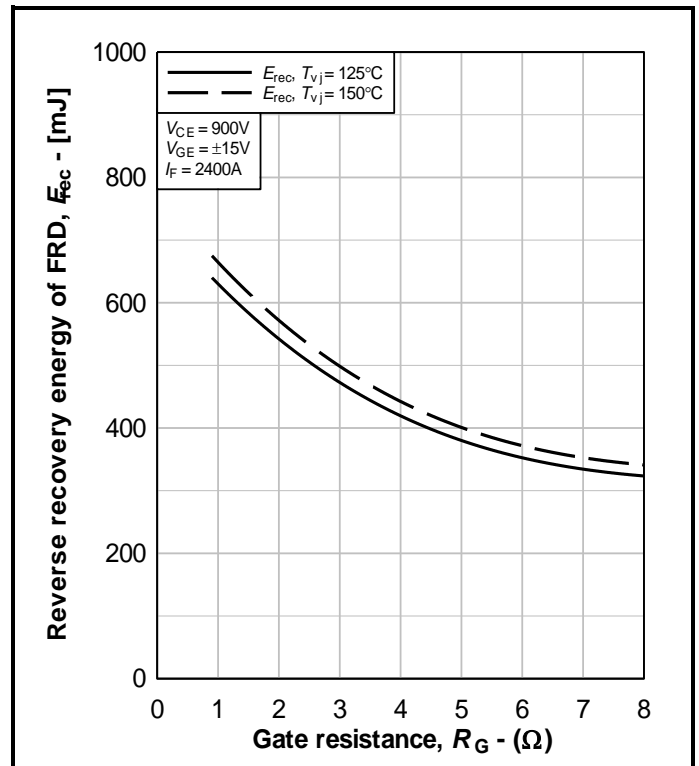


Fig. 10 Typical FRD E_{rec} , $E_{rec} = f(R_G)$

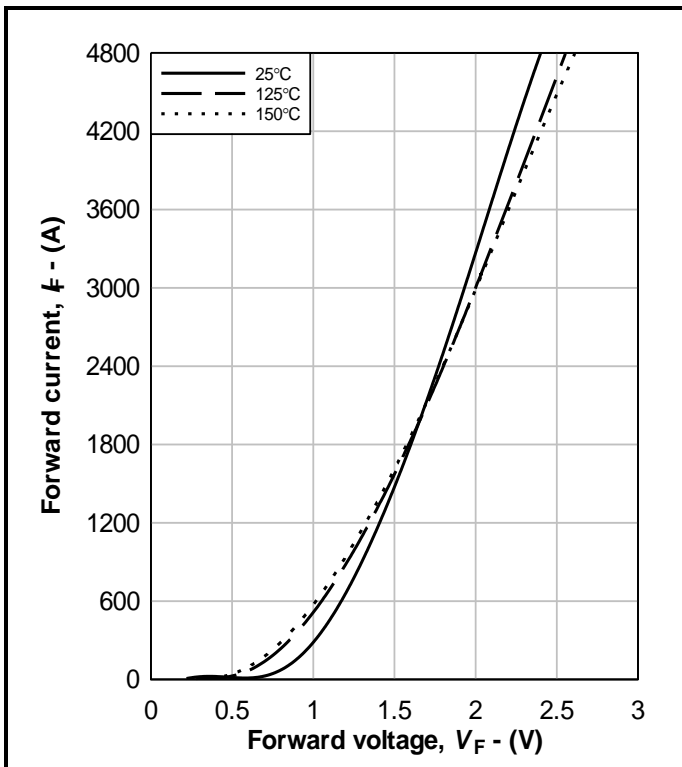


Fig. 11 Typical FRD output characteristics, $I_F = f(V_F)$

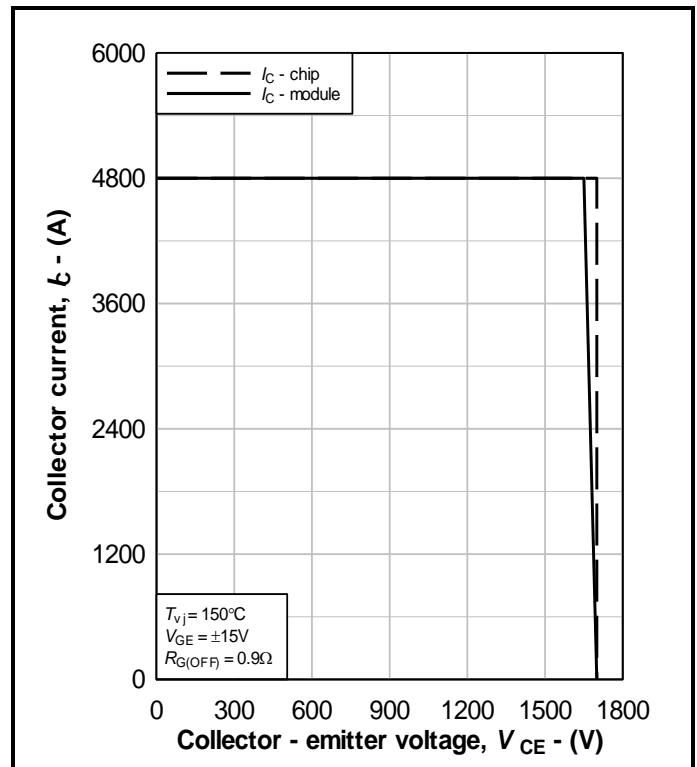


Fig. 12 Reverse bias safe operating area of IGBT, $I_C = f(V_{CE})$

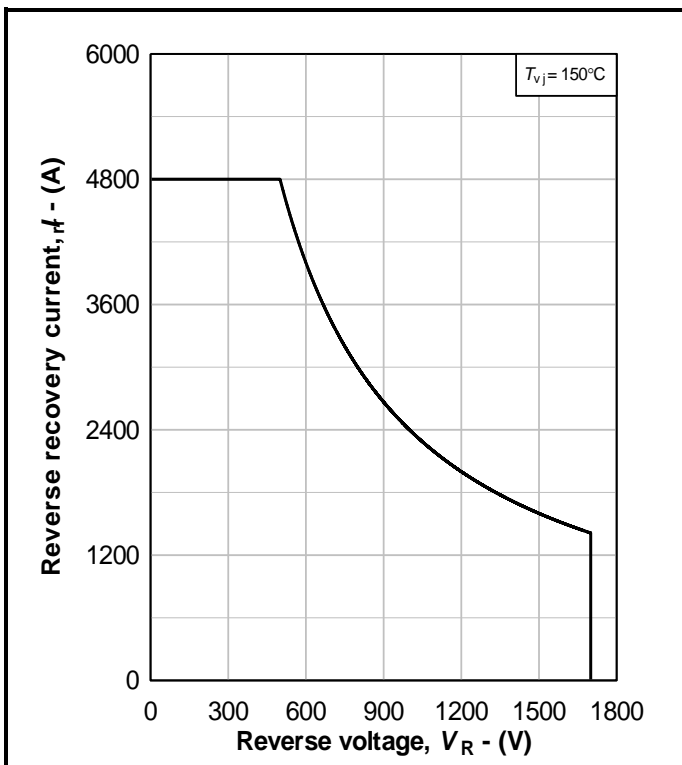


Fig. 13 Diode reverse bias safe operating area

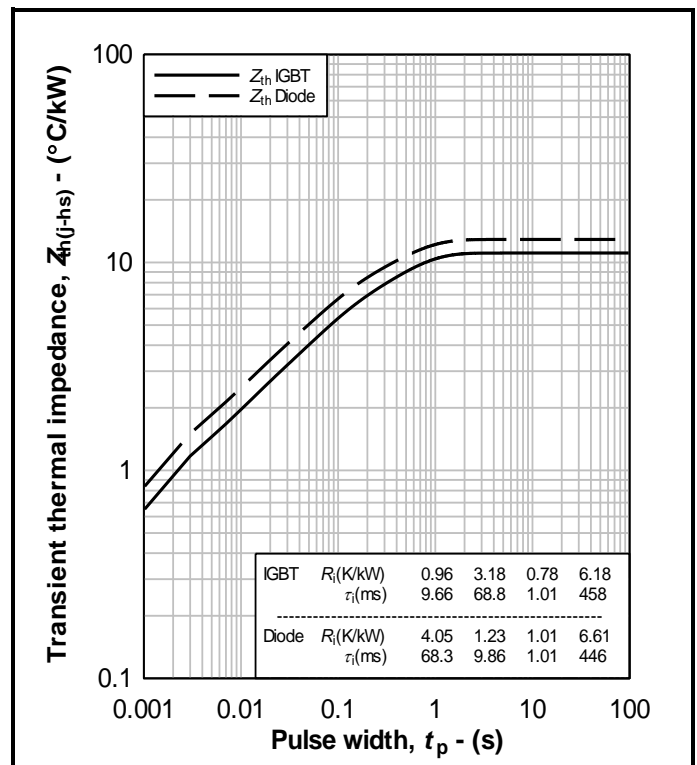


Fig. 14 Transient thermal impedance, $Z_{th(j-hs)} = f(t)$

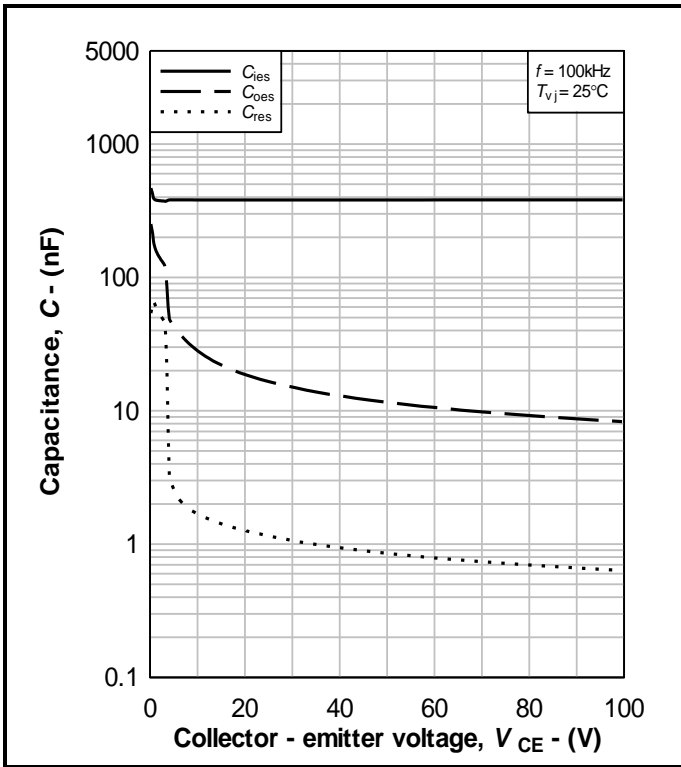


Fig. 15 Typical capacitor characteristic, $C = f(V_{CE})$

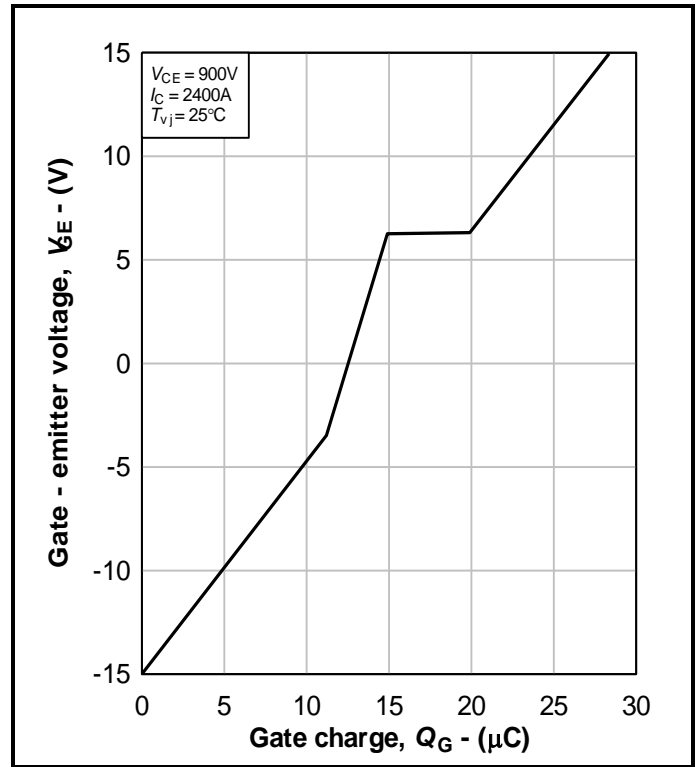
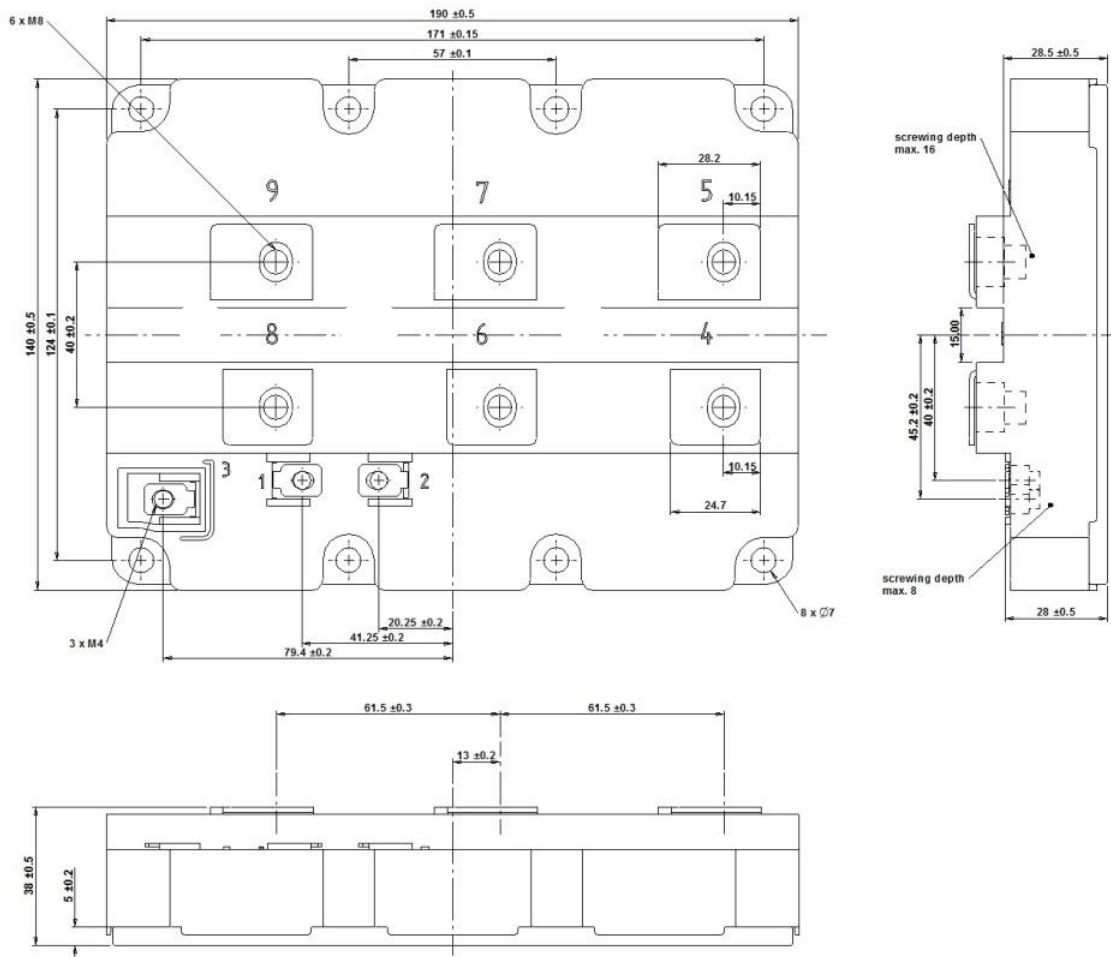


Fig. 16 Typical gate charge characteristic, $V_{GE} = f(Q_G)$

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services.
All dimensions in mm, unless stated otherwise.

DO NOT SCALE.



Nominal Weight: 1070g

Module Outline Type Code: E

Fig. 17 Module outline drawing

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